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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/575,890	05/22/2000	Te-Hsiu Tsai	JCLA 5861	2752

7590 07/16/2002
J.C. PATENTS INC.
4 VENTURE
SUITE 250
IRVINE, CA 92618

EXAMINER

ABDULSELAM, ABBAS L

ART UNIT	PAPER NUMBER
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2674

DATE MAILED: 07/16/2002

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Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/575,890

Applicant(s)

TSAI ET AL.

Examiner

Abbas I Abdulsalam

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 29 April 2002.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-27 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-27 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- 11) ☐ The proposed drawing correction filed on _____ is: a) ☐ approved b) ☐ disapproved by the Examiner.
- If approved, corrected drawings are required in reply to this Office action.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.
- 14) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).
- a) ☐ The translation of the foreign language provisional application has been received.
- 15) ☐ Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____.
- 4) ☐ Interview Summary (PTO-413) Paper No(s). _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

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DETAILED ACTION

Claim Rejections 35 U.S.C. 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Pariza et al. (USPN 6268845) in view of Huppenthal et al. (USPN 6339819).

Regarding claims 1, 13 and 25, Pariza teaches a monitor controller and USB controller for adjusting monitor attributes in any way desired and for communicating with the base system respectively. See column 3, lines 10-11. Pariza teaches a processor (100), A/D convertor (140), a ROM (134), and Memory/PCI/Cache controller (108) which provides a data path to a main memory (112). See column 4, lines 47-53 and Fig 1. Pariza teaches a USB monitor controller (302) along with a USB hub /peripheral controller (300) which provides a communication link and receives signals for passing to the base system. (B) via the USB (126). See column 9, lines 6-16 and see Fig 3-4. Furthermore, Pariza teaches Port 3 (320) which supplies common signal (322) in order to turn the LED "ON" and "OFF". Pariza further teaches that the controller is capable of writing and reading data. See column 10, lines 16-25 and 25-33. Pariza teaches a Universal Host Control Driver, UHCD.SYS (420) as well as data structures which are built in a memory (112) containing all the necessary information to provide end to end communication. See column 12,

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lines 3-12. However, Pariza does not teach a ROM such that data can be updated according to the address signal, data signal, and erase/read/write. However, Huppenthal teaches electrically erasable programmable ROMs which can be updated, enabling to perform new functions. See column 10, lines 42-57 and Fig 7.

Therefore, it would have been obvious to one having skill in the art at the time the invention was made modify Pariza's computer system to include Huppenthal's electrically erasable programmable ROMs. One would have been motivated in view of the suggestion in Huppenthal that the programmable ROM performs the desired updating of data. The use of a programmable ROM helps a multi processor computer architecture as taught by Huppenthal.

Regarding claim 2, Pariza teaches the base system in connection to recording message and sending a message over the USB so as to announce the state of the message via a controller. See column 3, lines 58-63.

Regarding claims 3 and 14, Pariza teaches a computer system and USB (126) which allows computer peripherals to be configured. See column 5, lines 56-65.

Regarding claims 4 and 15, Pariza teaches USB monitor device driver (406) with a ring (3) interface. See column 11, lines 17-20.

Regarding claims 5 and 17, Pariza teaches bezel button controls which controls host computer system using LED to check the status. Pariza also teaches USB driver (414) which operates to pass commands to a certain location. See column 3, lines 1-2 and column 11, lines 38-40.

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Regarding claims 6, 18 and 26-27, Pariza teaches a monitor controller for adjusting monitor attributes. See column 3, lines 10-13. Pariza teaches a monitor micro controller (302) providing monitor sleep signal (312) which controls LED (214). See Table 1 (column 7) and column 9, lines 39-45.

Regarding claims 7 and 19, Pariza teaches (USB) interface for base system connection and a digital converter (140) which receives composite video. See column 5, lines 5-7 and Fig 1.

Regarding claims 8, 16 and 20, Pariza teaches a processor (100), USB (122, 124), I/O controller coupled with ROM and memory (112). See Fig 1.

Regarding claims 9, 21 Pariza teaches an audio controller (152) which includes 16 bit stereo wave coder/decoder (CODEC), digital-analog converter (DAC) and a hardware master volume control. See column 5, lines 38-45.

Regarding claims 10 and 22, Pariza teaches a universal host control driver (420) along with a host controller (118) and a memory (112). See column 12, lines 3-12 and Fig 5.

Regarding claims 11-12 and 23-24, Pariza teaches the use of flash ROM and CD ROM (134). See column 3, lines 56-59 and Fig 1 respectively.

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Conclusion

2. The prior art made of record and not relied upon is considered to applicant's disclosure.

The following arts are cited for further reference

U.S. Pat. No. 5,835,761 to Ishii et al.

U.S. Pat No. 6,137,243 to Fujimori et al.

U.S. Pat No. 6,141,489 to Honda

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3. Any inquiry concerning this communication or earlier communication from the examiner should be directed to **Abbas Abdulsalam** whose telephone number is **(703) 305-8591**. The examiner can normally be reached on Monday through Friday (9:00-5:30).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Richard Hjerpe**, can be reached at **(703) 305-4709**.

Any response to this action should be mailed to:

Commissioner of patents and Trademarks

Washington, D.C. 20231

or faxed to:

(703) 872-9314

Hand delivered responses should be brought to crustal park II, Crystal Drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology center 2600 customer Service office whose telephone number is (703) 306-0377.



RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600

Abbas Abdulsalam

Examiner

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